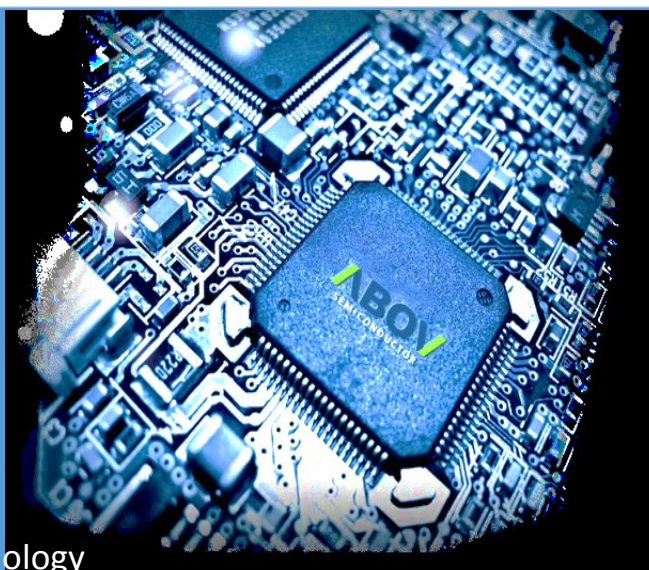


ERRATA AND DATASHEET CLARIFICATION

Leader of Microcontroller Technology
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Abstract	This errata and Datasheet Clarification describe the known functional problem.

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1. Stuck by UART Rx on flash erase/write operation

The UART block may be stuck if the UART Rx is received on flash erase/write operation. So, be aware of the following:

1. The RXEn bit of UARTnCR2, USTnCR2, and USInCR2 registers should be disabled before flash memory erase and write start.
2. After a flash erase and write operation.
3. If needed, the RXEn bit should be enabled by s/w again.

Where n is different for each device.